Transfer Agreement Renewal
University of Wisconsin-Platteville Master of Science in Engineering and
University of California-Irvine Division of Continuing Education

Embedded Systems Engineering Certificate

Completion of the UC-Irvine Division of Continuing Education Embedded Systems Engineering Certificate (hereinafter referred to as “the certificate”) equates to 9 graduate elective credits upon admission to the University of Wisconsin-Platteville Master of Science in Engineering program (hereinafter referred to as “the program”), under the following stipulations:

1) The certificate must be completed before the student is admitted to the program at UW-Platteville.

2) A grade of “B” or better must be earned in all certificate coursework to be eligible for transfer into the program at UW-Platteville.

3) The student must meet the certificate requirements which are outlined in Attachment A to receive nine graduate elective credits in the program at UW-Platteville.

4) The agreement is honored for the following emphasis areas:
   a. Engineering Design, Applications in Engineering Management or Control Systems

5) Under this agreement, both parties may advertise this articulation agreement to their students, prospective students and alumni through print online and interactive marketing materials such as fliers, websites and other advertisements, subject to the guidelines and restrictions further described in this section.
   a. Each party shall provide the other with a high-resolution logo file and hereby grants the other license to use the name and/or logo for these limited purposes.
   b. Use of the University of Wisconsin-Platteville name shall be “University of Wisconsin-Platteville” in all communications. Shortened versions such as “UW-Platteville” or “UWP” shall not be used.
   c. Use of UCI Division of Continuing Education name: In all communications as described above, the entity name shall be “UCI Division of Continuing Education” or “UCI Continuing Education”. It is not appropriate to use other variations of the University’s name, such as “University of California” or “UC Irvine”.
   d. Prior written approval for any uses of the UC Irvine Division of Continuing Education name or logo beyond those explicitly stated above must be obtained from the UC Irvine Extension director of marketing or designee.
   e. Prior written approval must be obtained by the issuing party in advance of issuance of press release(s) featuring the name and/or logo of the other party.

6) This agreement is subject to review every three years to affirm that it is achieving its intended purpose and meeting the needs of both institutions unless curriculum changes warrant earlier review. Please note that either party has the right to terminate this agreement upon 30 days’ notice. Any cancellation of this agreement shall provide for students active, at the time of cancellation, to complete any program in progress at that time.
Transfer Agreement Renewal
University of Wisconsin-Platteville Master of Science in Engineering and
University of California-Irvine Division of Continuing Education
Embedded Systems Engineering Certificate

Philip J. Parker
Dr. Philip Parker, Program Coordinator
Master of Science in Engineering, UW-Platteville

Molly M. Gribb
Dr. Molly Gribb
Dean of Engineering Mathematics and Science

Craig Wilson
Dr. Craig Wilson
Dean of Distance, Continuing and Graduate Education

Dr. D. Joanne Wilson,
Provost and Vice Chancellor of Academic Affairs, UW-Platteville

Gary W. Matkin, Ph.D.
Dean, Continuing Education
Vice Provost, Career Pathways
Transfer Agreement Renewal
University of Wisconsin-Platteville Master of Science in Engineering and
University of California-Irvine Division of Continuing Education
*Embedded Systems Engineering Certificate*

Attachment A

<table>
<thead>
<tr>
<th>Course #</th>
<th>Required Course Name</th>
<th>Quarter Units</th>
</tr>
</thead>
<tbody>
<tr>
<td>EECS X497.32</td>
<td>Fundamentals of Embedded Systems Design and Programming</td>
<td>3</td>
</tr>
<tr>
<td>EECS X497.36</td>
<td>Embedded Systems Architecture</td>
<td>3</td>
</tr>
<tr>
<td>EECS X497.34</td>
<td>Real-Time Embedded Systems Programming</td>
<td>3</td>
</tr>
<tr>
<td>Course #</td>
<td>Elective Course Name (minimum 6 units)</td>
<td>Quarter Units</td>
</tr>
<tr>
<td>EECS X494.92</td>
<td>Logic Design and Analysis using Verilog</td>
<td>3</td>
</tr>
<tr>
<td>EECS X494.94</td>
<td>VHDL Design and Modeling of Digital Systems</td>
<td>3</td>
</tr>
<tr>
<td>EECS X494.95</td>
<td>FPGA Design with Hardware Description Languages</td>
<td>3</td>
</tr>
<tr>
<td>EECS X437.6</td>
<td>Motor Control Algorithms and Application</td>
<td>4</td>
</tr>
<tr>
<td>EECS X497.19</td>
<td>Writing Portable Device Drivers</td>
<td>3</td>
</tr>
<tr>
<td>EECS X497.31</td>
<td>Designing Embedded Software Using Real-time Operating Systems</td>
<td>3</td>
</tr>
<tr>
<td>EECS X497.10</td>
<td>Fundamentals of Embedded Linux</td>
<td>3</td>
</tr>
<tr>
<td>EECS X497.11</td>
<td>Linux Driver Primer</td>
<td>1.5</td>
</tr>
<tr>
<td>EECS X497.39</td>
<td>Embedded Systems Design Using ARM Technology</td>
<td>3</td>
</tr>
<tr>
<td>EECS X497.4</td>
<td>Applied Control Theory for Embedded Systems</td>
<td>3</td>
</tr>
<tr>
<td>EECS X498.61</td>
<td>Real-Time Embedded Digital Signal Processing</td>
<td>3</td>
</tr>
</tbody>
</table>
"ENG_UC-Irvine_Embedded Systems_March 2018_signed by UCI" History

Document uploaded by Katharine Caywood (caywoodk@uwplatt.edu) from Acrobat
03/28/2018 - 8:54:08 AM PDT- IP address: 137.104.117.76

Document emailed to Philip J. Parker (parkerp@uwplatt.edu) for signature
03/28/2018 - 8:57:13 AM PDT

Document viewed by Philip J. Parker (parkerp@uwplatt.edu)
03/28/2018 - 1:50:50 PM PDT- IP address: 137.104.94.124

Document e-signed by Philip J. Parker (parkerp@uwplatt.edu)
Signature Date: 28/03/2018 - 13:51:18 PDT - Time Source: server- IP address: 137.104.94.124

Document emailed to Molly M. Gribb (gribbm@uwplatt.edu) for signature
28/03/2018 - 13:51:19 PDT

Document viewed by Molly M. Gribb (gribbm@uwplatt.edu)
28/03/2018 - 14:03:34 PDT- IP address: 208.184.162.163

Document e-signed by Molly M. Gribb (gribbm@uwplatt.edu)
Signature Date: 30/03/2018 - 06:30:03 PDT - Time Source: server- IP address: 184.158.135.138

Document emailed to Craig Wilson (wilsoncraig@uwplatt.edu) for signature
30/03/2018 - 06:30:05 PDT

Document viewed by Craig Wilson (wilsoncraig@uwplatt.edu)
30/03/2018 - 07:46:09 PDT- IP address: 137.104.191.96